

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Active ESD Shunt with Transistor Feedback to Reduce Latch-Up Susceptibility						
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Paul Tong</p> <p>Attorney Docket Number: PS-103</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (5255146 or 6552886 or 5559659 or 6242942 or 5946177 or 6522511 or 5744842 or 5745323 or 6552583 or 6552886 or 6118640).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DHP	1	5255146	2003-09-22	Miller	B1	361	056
DHP	2	6552886	2003-04-22	Wu et al.	B1	361	056
DHP	3	5559659	1996-09-24	Strauss et al.	B1	361	056
DHP	4	6242942	2001-06-05	Shamarao	B1	326	027
DHP	5	5946177	1999-08-31	Miller et al.	B1	361	056
DHP	6	6522511	2003-03-18	John et al.	B1	361	056
DHP	7	5744842	1998-03-28	Ker	B1	257	362
DHP	8	5745323	1998-04-28	English et al.	B1	361	056
DHP	9	6552583	2003-04-23	Kwong	B1	327	112
DHP	10	6552886	2003-04-23	Wu et al.	B1	361	056
DHP	11	6118640	2000-09-12	Kwong	B1	361	056
Signature							
Examiner Name				Date			
Dharti Patel				7/11/05			